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2891

**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Confirmation No.: 8576

Nobuaki HASHIMOTO

Allowed: August 16, 2005

Group Art Unit: 2891

Application No.: 09/486,561

Examiner: D. ZARNEKE

Filed: February 29, 2000

Docket No.: 105030

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF,  
CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

**LETTER TO THE OFFICIAL DRAFTSPERSON**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please substitute the attached first sheet of formal drawings depicting Figure 1 for the corresponding drawings filed with the application. Figure 1(D) has been corrected in accordance with the Request for Approval of Drawing Correction filed May 29, 2001.

Respectfully submitted,

James A. Oliff  
Registration No. 27,075

Leana Levin  
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JAO:CWB:LXL/tlp  
Date: November 15, 2005

**OLIFF & BERRIDGE, PLC**  
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